

SC10098C  
PATENT

S.N. 09/709,893

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jefferson W. Hall et al  
Date: December 12, 2003  
Serial No.: 09/709,893  
Filed: November 13, 2000  
For: POWER CONVERSION INTEGRATED  
CIRCUIT ND METHOD FOR  
PROGRAMMING  
Group: 2838  
Examiner: Rajnikant B. Patel

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## AMENDMENT

HONORABLE COMMISSIONER OF PATENTS AND TRADEMARKS,  
P.O. Box 1450  
Alexandria, VA 22313-1450

SIR:

In response to the Office Action mailed June 18, 2003  
please reconsider the above-identified patent application in  
view of the amendments and remarks presented hereinafter.

A request for a three month extension of time from  
September 18, 2003 to December 18, 2003 is filed  
concurrently herewith.